Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L13	46	DTx with (controller processor)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:13
L14	21	13 with ((random adj bit) command signal)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:12
L15	1256	discontinuous$4 with (transmit$4 modulat44)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:13
L16	48	15 with (controller processor)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/20 15:13

	Type	Hits	Search Text
84	BRS	5530	370/389
85	BRS	789	370/92
86	BRS	5182	370/392
87	BRS	2935	370/400
88	BRS	3524	370/468
89	BRS	4175	370/465
90	BRS	78	S87 and S88
91	BRS	59	S87 and S90
92	BRS	14	S87 and S92
93	BRS	104	S94 S95
94	BRS	101	S97 and (tag coherent type)
95	BRS	5	S97 and (tag and coherent and type)
96	BRS	5	S97 and (tag and coherent)
97	BRS	6	S97 and (coherent)
98	IS&R	2	("6763012").PN.
99	BRS	1810	discontinuous$4 with (transmit$4 modulat$4)
100	BRS	49	S103 with (controller system processor CPU) with slot
101	BRS	1924	(discontinuous$4 DTx) with (transmit$4 modulat$4)
102	BRS	1409	370/321
103	BRS	2955	370/329
104	BRS	1277	370/336

	Type	Hits	Search Text
105	BRS	2764	370/347
106	BRS	46	S105 and S106
107	BRS	88	S105 and S107
108	BRS	61	S105 and S108
109	BRS	101	S105 and S109
110	BRS	156	S110 S111 S112 S113
111	BRS	32	S114 and S115
112	BRS	326414	(controller system processor CPU computer) with generat$4 with ((random adj bit) signal command)